Model 855 Dual Spectroscopy Amplifier Operating and Service Manual

This manual applies to instruments marked "Rev. 03" on rear panel.

Advanced Measurement Technology, Inc.

a/k/a/ ORTEC[®], a subsidiary of AMETEK[®], Inc.

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Shipments should be examined immediately upon receipt for evidence of external or concealed damage. The carrier making delivery should be notified immediately of any such damage, since the carrier is normally liable for damage in shipment. Packing materials, waybills, and other such documentation should be preserved in order to establish claims. After such notification to the carrier, please notify ORTEC of the circumstances so that assistance can be provided in making damage claims and in providing replacement equipment, if necessary.

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SAFETY INSTRUCTIONS AND SYMBOLS

This manual contains up to three levels of safety instructions that must be observed in order to avoid personal injury and/or damage to equipment or other property. These are:

- **DANGER** Indicates a hazard that could result in death or serious bodily harm if the safety instruction is not observed.
- **WARNING** Indicates a hazard that could result in bodily harm if the safety instruction is not observed.
- **CAUTION** Indicates a hazard that could result in property damage if the safety instruction is not observed.

Please read all safety instructions carefully and make sure you understand them fully before attempting to use this product.

In addition, the following symbol may appear on the product:





Please read all safety instructions carefully and make sure you understand them fully before attempting to use this product.

SAFETY WARNINGS AND CLEANING INSTRUCTIONS

DANGER Opening the cover of this instrument is likely to expose dangerous voltages. Disconnect the instrument from all voltage sources while it is being opened.

WARNING Using this instrument in a manner not specified by the manufacturer may impair the protection provided by the instrument.

Cleaning Instructions

To clean the instrument exterior:

- Unplug the instrument from the ac power supply.
- Remove loose dust on the outside of the instrument with a lint-free cloth.
- Remove remaining dirt with a lint-free cloth dampened in a general-purpose detergent and water solution. Do not use abrasive cleaners.

CAUTION To prevent moisture inside of the instrument during external cleaning, use only enough liquid to dampen the cloth or applicator.

• Allow the instrument to dry completely before reconnecting it to the power source.



ORTEC MODEL 855 DUAL SPECTROSCOPY AMPLIFIER

1. DESCRIPTION

1.1. GENERAL

The ORTEC 855 Amplifier is two spectroscopy amplifiers in a single-width NIM module that feature a versatile combination of jumper-selectable pulse shaping characteristics. The amplifier has extremely low noise, a wide gain range, and excellent overload response for universal application in high-resolution spectroscopy. It accepts input pulses of either polarity. Input may originate from germanium or silicon semiconductor detectors, from scintillation counters with either fast or slow scintillators, from proportional counters, from pulsed ionization chambers, from electron multipliers, etc.

The unit has an input impedance of ~1000 Ω and accepts either positive or negative input pulses with rise times <650 ns and fall times >30 µs. Three integration and differentiation time constants are jumper-selectable on the printed wiring board (PWB) to provide optimum shaping for resolution and count rate. The differentiation network has variable pole-zero cancellation that can be adjusted to match preamplifiers with decay times >30 µs. The pole-zero cancellation drastically reduces the undershoot after the differentiator and greatly improves overload and count rate characteristics. In addition, the amplifier contains an active, filter-shaping network that optimizes the signal-to-noise ratio and minimizes the overall resolving time.

The amplifier has unipolar and bipolar BNC outputs. The unipolar output is used for spectroscopy in systems where do coupling can be maintained from the 855 to the analyzer. A BLR (baseline restorer) circuit is included in the unit for improved performance at all count rates. Baseline correction is applied only during intervals between input pulses and automatically selects a discriminator level to identify input pulses. The unipolar output do level is within the range of -5 mV to +5 mV. This output permits the use of the direct-coupled input of the analyzer with a minimum amount of interface problems.

The 855 can be used for constant-fraction timing when operated in conjunction with an ORTEC 551,552, or 553 Timing Single-Channel Analyzer. The ORTEC Timing Single-Channel Analyzers feature a minimum of walk as a function of pulse amplitude and incorporate a variable delay time on the output pulse to enable the timing pickoff output to be placed in time coincidence with other signals.

The 855 has complete provisions, including power distribution, for operating any ORTEC solid-state preamplifier. Normally. the preamplifier pulses should have a rise time of 0.25 μ s or less to properly match the amplifier filter network and a decay time >30 μ s for proper pole-zero cancellation. The input impedance is 1000 Ω .

When long preamplifier cables are used, the cables can be terminated in series at the preamplifier end or in shunt at the amplifier end with the proper resistors. The output impedance is -0.2Ω and the output can be connected to other equipment by a single cable going to all equipment. The cable must be shunt-terminated at the far end. (See Section 3 for further information.)

1.2. POLE-ZERO CANCELLATION

Pole-zero cancellation is a method for eliminating pulse undershoot after the differentiating network. In an amplifier not using pole-zero cancellation (Fig. 1.1.) the exponential tail on the preamplifier output signal (usually 50 to 500 μ s) causes an undershoot whose peak amplitude is roughly determined from:

undershoot amplitude differentiated pulse amplitude

> = <u>differentiation time</u> preamplifier pulse decay time.

For a I-µs differentiation time and a 50-µs pulse decay time the maximum undershoot is 2%, and this decays with a 50-µs time constant. Under overload conditions, this undershoot is often sufficiently large to saturate the amplifier during a

considerable portion of the undershoot, causing excessive dead time. This effect can be reduced by increasing the preamplifier pulse decay time (which generally reduces the counting rate capabilities of the preamplifier) or compensating for the undershoot by providing pole-zero cancellation.

Pole-zero cancellation is accomplished by the network shown in Fig. 1.2. The pole $(s + (1/T_o))$ due to the preamplifier pulse decay time is canceled by the zero of the network $[s + (k/R_2C_1)]$. In effect, the dc path across the differentiation capacitor adds an attenuated replica of the preamplifier pulse to just cancel the negative undershoot of the differentiating network.

Total preamplifier-amplifier pole-zero cancellation requires that the preamplifier output pulse decay time be a single exponential decay and be matched to the pole-zero cancellation network. The variable pole-zero cancellation network allows accurate cancellation for all preamplifiers having 30-µs or greater decay times. Improper matching of the pole-zero network will degrade the overload performance and cause excessive pileup distortion at medium counting rates. Improper matching causes either an under-compensation (undershoot is not eliminated) or an overcompensation (output after the main pulse does not return to the baseline but decays to the baseline with the preamplifier time constant). The pole-zero adjust is accessible on the front panel of the 855 and can easily be adjusted by observing the baseline on an oscilloscope with a monoenergetic source or pulser having the same decay time as the preamplifier under overload conditions. The adjustment should be made so that the pulse returns to the baseline in the minimum time with no undershoot.

1.3. ACTIVE FILTER

When only FET gate current and drain thermal noise are considered, the best signal-to-noise ratio occurs when the two noise contributions are equal for a given input pulse shape. The gaussian pulse shape (Fig. 1.3.) for this condition requires an amplifier with a single RC differentiation and n equal RC integrations where n approaches infinity. The Laplace transform of this transfer function is

$$G(s) = \frac{s}{s+(1/RC)} \times \frac{1}{[s+(1/RC)]^n} (n - \infty)$$

where the first factor is the single differentiation, and the second factor is the n integrations. The active filter approximates this transfer function.



Fig. 1.1. Differentiation in an Amplifier Without Pole-Zero Cancellation.





Figure 1.3. illustrates the results of pulse shaping in an amplifier. Of the four pulse shapes shown the cusp would produce minimum noise, but this is impractical to achieve with normal electronic circuitry and would be difficult to measure with an ADC. The true gaussian shape deteriorates the signal-to-noise ratio by only about 12% from that of the cusp and produces a signal that is easy to measure but requires many sections of integration $(n \rightarrow \infty)$. With two sections of integration the waveform identified as a gaussian approximation can be obtained, and this deteriorates the signal-tonoise ratio by about 22%. The ORTEC active filter network in the 855 provides a fourth waveform in Fig. 1.3.; this waveform has characteristics superior to the n = 2 gaussian approximation, yet obtains them with two complex poles and a real pole. By this method, the output pulse shape has a good signal-to-noise ratio, is easy to calculate, and yet requires only a practical amount of electronic circuitry to achieve the desired results.



Fig. 1.3. Pulse Shapes for Good Signal-to-Noise Ratios.

2. SPECIFICATIONS

The ORTEC 855 Amplifier is two spectroscopy amplifiers in a single-width NIM module. Only one amplifier will be referred to in the discussion. All references, such as jumper settings, input/output connections, specifications, etc., apply to both amplifiers.

2.1. PERFORMANCE

GAIN RANGE Continuously adjustable from 5 to 1250.

PULSE SHAPE Semigaussian on all ranges with peaking time equal to 2.2τ , 50% pulse width equal to 3.3τ , and pulse width at 0.1% level equal to 4.0 times the peaking time. Bipolar crossover = 1.5τ .

INTEGRAL NONLINEARITY $<\pm 0.05\%$ for 1.5-µs shaping time.

NOISE <5 μ V rms referred to the input using 3 μ s unipolar shaping; <7 μ V using 1.5. μ s shaping; both for again \ge 100.

TEMPERATURE INSTABILITY

Gain ≤±0.0075%/°C, 0 to 50°C. DcLevel ≤±30 μ V/°C, 0 to 50°C.

WALK $\leq \pm 5$ ns at 0.5 µs shaping for 50:1 dynamic range including contribution of an ORTEC 551 or 552 Constant-Fraction Timing Single-Channel Analyzer.

OVERLOAD RECOVERY Recovers to within 2% of rated output from X300 overload in 2.5 nonoverload pulse widths using maximum gain for unipolar output. Same recovery from X500 overload for bipolar.

RESTORER Gated active baseline stabilizer with automatic threshold circuit to provide the threshold level as a function of signal noise to the baseline restorer discriminator.

SPECTRUM BROADENING Typically <10% FWHM for a ⁶⁰Co 1.33-MeV gamma line at 85% of full scale for an incoming count rate of 1 to 50k cps. Unipolar output, 1.5 µs shaping.

SPECTRUM SHIFT¹ Peak position shifts typically <0.02% for a ⁶⁰Co 1.33-MeV gamma line at 85% of full scale (measured at the unipolar output, 1.5 μs shaping, 1 to 50k cps).

2.2. CONTROLS

FINE GAIN Ten-turn precision potentiometer with graduated dial for continuously variable direct-reading gain factor of X2.5 to X12.5.

COARSE GAIN Six-position switch selects feedback resistors for gain factors of 2, 4, 10, 20, 40, and 100.

SHAPING TIME Three-position printed wiring board (PWB) jumpers, easily accessible through side panel, select time constants for active pulse-shaping filter network of 0.5, 1.5, or 3 µs.

POS/NEG A PWB jumper (W3) selects either positive or negative input polarity.

PZADJ Screwdriver adjustable potentiometer to set the pole-zero cancellation to compensate input decay times from 30 μ s to ∞ .

2.3. INPUT

INPUT BNC (UG-1094A/U) front and rear panel connectors accept either positive or negative pulses with rise times of 10 to 650 ns and decay times of 30 µs to ∞ , $Z_{in} = 1000 \Omega$ dc-coupled, linear maximum 2 V; absolute maximum 20 V.

2.4. OUTPUTS

UNI Front panel BNC connector with $Z_o < 1 \Omega$. Short-circuit proof; full-scale linear range of 0 to +10 V; active filter shaped; dc-restored with dc level adjustable to ±15 mV.

BI Front panel BNC connector with $Z_o < 1$. Shortcircuit proof; positive lobe leading and full-scale linear range of 0 to +10 V; active filter shaped.

¹These count rate specifications were measured with a 10% HPGe detector. Detectors with a large number of slow, rise-time signals will most likely give poorer results.

Rear panel BNC connector with $Z_o = 93\Omega$. PWB jumper (W8) selects either unipolar or bipolar rear panel output.

PREAMP POWER Rear panel standard ORTEC power connector (Amphenol 17-10090) mates with captive and noncaptive power cords on all ORTEC preamplitiers.

2.5. RELATED EQUIPMENT

The ORTEC 855 Amplifier accepts linear pulses from, and furnishes power to, any standard ORTEC preamplifier or equivalent. Its output pulses may be used for linear signal analysis, using any of the ORTEC modular instruments and multichannel analyzers.

2.6. ELECTRICAL AND MECHANICAL

POWER REQUIRED +24V, 83mA; 24V, 70mA; +12V, 125 mA; 12 V, 140 mA.

WEIGHT

Net 1.5 kg (3.3 lb). Shipping 3.1 kg (7.0 lb).

DIMENSIONS Standard single-width NIM module 3.43 x 22.13 cm (1.35 x 8.714 in.) per TID-20893 (Rev).

3. INSTALLATION

The ORTEC 855 Amplifier is two spectroscopy amplifiers in a single-width NIM module. Only one amplifier will be referred to in the discussion. All references, such as jumper settings, input/output connections, specifications, etc., apply to both amplifiers.

3.1. GENERAL

The 855 operates on power that must be furnished from a NIM-standard bin and power supply such as the ORTEC 400IA/4002A Series. The bin and power supply is designed for relay rack mounting. If the equipment is to be rack mounted, be sure that there is adequate ventilation to prevent any localized heating of the components used in the 855. The temperature of equipment mounted in racks can easily exceed the maximum limit of 50 °C unless precautions are taken.

3.2. CONNECTION TO POWER

The 855 contains no internal power supply and must obtain the necessary dc operating power from the bin and power supply in which it is installed for operation. Always turn off power for the power supply before inserting or removing any modules. After all modules have been in- stalled in the bin and any preamplifiers have also been connected to the Preamp Power connectors on the amplifiers, cheek the dc voltage levels from the power supply to see that they are not overloaded. The ORTEC 4001A/4002A Series Bins and Power Supplies have convenient test points on the power supply control panel to permit monitoring these dc levels. If any one or more of the dc levels indicates an overload, some of the modules will need to be moved to another bin to achieve operation.

3.3. CONNECTION TO PREAMPLIFIER

The preamplifier output signal is connected to the 855 through the Input BNC connectors on the front and rear panels. The input impedance is ~1000 Ω and is dc-coupled to ground; therefore, the preamplifier output must be either ac-coupled or have approximately zero dc voltage under no-signal conditions.

The 855 incorporates pole-zero cancellation to enhance the overload and count rate characteristics of the amplifier. This technique requires matching the network to the preamplifier decay-time constant to achieve perfect compensation. The pole-zero adjustment should be set each time the preamplifier or the shaping time constant of the amplifier is changed. For details of the pole-zero adjustment see Section 4.5. An alternate method is accomplished easily by using a monoenergetic source and observing the amplifier baseline with an oscilloscope after each pulse under ~X2 overload conditions. Adjustment should be made so that the pulse returns to the baseline in a minimum amount of time with no undershoot.

Preamplifier power at +24 V, -24 V, +12 V, and -12 V is available through the Preamp Power connector on the rear panel. When the preamplifier is connected, its power requirements are obtained from the same bin and power supply as is used for the amplifier, and this increases the dc loading on

each voltage level over and above the requirements for the unit at the module position in the bin.

When the 855 is used with a remotely located preamplifier (i.e., preamplifier-to-amplifier connection through 25 ft or more of coaxial cable), be careful to ensure that the characteristic impedance of the transmission line from the preamplifier output to the 855 input is matched. Because the input impedance of the 855 is ~1000 Ω , sending-end termination will normally be preferred; the transmission line should be series-terminated at the preamplifier output. All ORTEC preamplifiers contain series terminations that are either 93 Ω or variable; coaxial cable type RG-62/U or RG-71/U is recommended.

3.4. CONNECTION OF TEST PULSE GENERATOR

THROUGH A PREAMPLIFIER The satisfactory connection of a test pulse generator (such as the ORTEC 419 Precision Pulse Generator or equivalent) depends primarily on two considerations: the preamplifier must be properly connected to the unit as discussed in Section 3.3, and the proper input signal simulation must be applied to the preamplifier. To ensure proper input signal simulation, refer to the instruction manual for the particular preamplifier being used.

DIRECTLY INTO THE 855 Since the input of the 855 has $1000-\Omega$ input impedance, the test pulse generator will normally have to be terminated at the amplifier input with an additional shunt resistor. In addition, if the test pulse generator has a dc offset, a large series isolating capacitor is also required because the 855 input is dc coupled. The ORTEC test pulse generators are designed for direct connection. When any one of these units is used, it should be terminated with a 100- Ω terminator at the amplifier input or be used with at least one of the output attenuators set at **In**. (The small error due to the finite input impedance of the amplifier can normally be neglected.)

SPECIAL CONSIDERATIONS FOR POLE-ZERO

CANCELLATION When a tail pulser is connected directly to the amplifier input, the PZ ADJ should be adjusted if overload tests are to be made (other tests are not affected). See Section 4.5 for the polezero adjustment. It a preamplifier is used and a tail pulser is connected to the preamplifier test input,

similar precautions are necessary. In this case the effect of the pulser decay must be removed; that is, a step input should be simulated.

3.5. SHAPING CONSIDERATIONS

The shaping time constant on the 855 is selectable by PWB-mounted jumpers in steps of 0.5, 1.5. and 3 µs. The choice of the proper shaping time constant is generally a compromise between operating at a shorter time constant for accommodation of high counting rates and operating with a longer time constant for a better signal-to-noise ratio. For scintillation counters, the energy resolution depends largely on the scintillator and photomultiplier, and therefore a shaping time constant of about four times the decay-time constant of the scintillator is a reasonable choice (for Nal, a 1.5-µs shaping time constant is about optimum). For gas proportional counters, the collection time is normally in the 0.5 to 5 µs range and a 1.5 us or greater time constant selection will generally give optimum resolution. For surface barrier semiconductor detectors, a 0.5- to 2-µs resolving time will generally provide optimum resolution. Shaping time for Ge(Li) detectors will vary from 1.5 to 6 µs, depending on the size, configuration, and collection time of the specific detector and preamplifier. When a charge-sensitive preamplifier is used, the optimum shaping time constant to minimize the noise of a system can be determined by measuring the output noise of the system and dividing it by the system gain. The 855 has almost constant gain for all shaping modes; therefore, the optimum shaping can be determined by measuring the output noise with a voltmeter as each shaping time constant is selected.

3.6. LINEAR OUTPUT CONNECTIONS AND TERMINATING CONSIDERATIONS

Since the 855 unipolar output is normally used for spectroscopy, the unit is designed with the flexibility to interface the pulse with an analyzer. A gated baseline restorer (BLR) circuit is included in this output for improved performance at all count rates. The threshold for the restorer gate is determined automatically, according to the input noise level. The unipolar output dc level is 0 ± 5 mV. Three general methods of termination are used. The simplest of these is shunt termination at the receiving end of the cable. A second method is series termination at the sending end. The third is a combination of series and shunt termination, where the cable impedance is matched both in

series at the sending end and in shunt at the receiving end. The combination is most effective, but this reduces the amount of signal strength at the receiving end to 50% of that which is available in the sending instrument.

To use shunt termination at the receiving end of the cable, connect the output from the 855 front or rear panels through 93- Ω cable to the input of the receiving instrument. Then use a BNC tee connector to attach both the interconnecting cable and a 100- Ω terminator at the input connector of the receiving instrument. Since the input impedance of the receiving instrument is normally 1000 Ω or more, the effective instrument input impedance with the 100- Ω terminator will be of the order of 93 Ω , and this will match the cable impedance correctly.

For customer convenience, ORTEC stocks the proper terminators and BNC tees, or they can be ordered from a variety of commercial sources.

3.7. SHORTING OR OVERLOADING THE AMPLIFIER OUTPUT

The 855 output is dc-coupled with an output impedance of ~0.2 Ω . If the output is shorted with a direct short circuit, the output stage will limit the peak current of the output so that the amplifier will not be harmed. When the amplifier is terminated with 100 Ω , the maximum count rate consistent with linear output is

$$RATE_{max} = \frac{125000 \, cps}{\tau} \, x \, \frac{10}{V_o}$$

where V_o is the peak output pulse amplitude in volts (V) and τ is the shaping time in μ s.

4. OPERATION

4.1. INITIAL TESTING AND OBSERVATION OF PULSE WAVEFORMS

Refer to Section 6 for information on testing performance and observing waveforms at front panel test points. Figure 4.1. shows typical output waveforms.



Fig. 4.1. Typical Unipolar and Bipolar Output Waveforms.

4.2. FRONT PANEL CONTROLS

GAIN A Coarse Gain switch and a Gain control (a precision 10-turn locking potentiometer) select and precisely adjust the gain factor for the amplification in the 855. Switch settings are X2, 4, 10, 20, 40, and 100. Continuous fine gain range is from XO.5 to X12.5 using markings of 500 through 1250 dial divisions.

Using these controls collectively, the gain can be set at any level from X5 through X1250.

POS/NEG A PWB jumper (W3) selects an input circuit that accepts either polarity of pulses from the preamplifier.

PZ ADJ A screwdriver control sets the pole-zero cancellation to match the preamplifier pulse decay characteristics. The range is from 30 μ s to ∞ .

SHAPING PWB jumpers select equal integration and differentiation time constants to shape the input pulses. Settings are 0.5, 1.5, and 3 μ s. Illustrated drawings and instructions for setting time-constant jumpers are shown in Fig. 4.2.



Fig. 4.2. Set Jumpers W1, W2, W4–W7, and W9 as Shown, in Both Channel 1 and Channel 2, to Select Desired Time Constant (figure shows setting of 1.5 μ s).

4.3. PANEL CONNECTIONS

INPUTS Accept input pulses to be shaped and/or amplified by the 855. Compatible characteristics; positive or negative with rise time from 10 to 650 ns; decay time >30 μ s for proper pole-zero cancellations; input linear amplitude range 0 to 2 V with a maximum limit of ±20 V. Input impedance is ~1000 Ω .

OUTPUTS

UNI Front panel BNC connector with $Z_o < 1 \Omega$. Short-circuit proof; full-scale linear range of 0 to +10 V; active filter shaped; dc-restored with dc level adjustable to ±15 mV.

BI Front panel BNC connector with $Z_o < 1 \Omega$. Shirtcircuit proof; positive lobe leading and full-scale linear range of 0 to +10 V; active filter shaped.

Rear panel BNC connector with $Z_{o} = 93 \Omega$. PWB jumper (W8) selects either bipolar or unipolar rear panel output.

PREAMP POWER Rear panel standard ORTEC power connector (Amphenol 17-10090) mates with captive and noncaptive power cords on all ORTEC preamplifiers.

4.4. STANDARD SETUP PROCEDURE

a. Connect the detector, preamplifier, highvoltage power supply, and preamplifier into a basic system and connect the amplifier output to an oscilloscope. Connect the preamplifier power cable to the Preamp connector on the 855 rear panel. Turn on power in the bin and power supply and allow the electronics of the system to warm up and stabilize.

b. Set the 855 controls initially as follows:

Shaping	1.5,µs
Coarse Gain	10
Fine Gain	5.00
Pos/Neg	Match input pulse polarity

- **c.** Use a ⁶⁰Co calibration source; place it about 25 cm from the active face of the detector. The unipolar output pulse from the 855 should be about 8 to 10 V using a preamplifier with a conversion gain (charge sensitivity) of 170 mV/MeV.
- **d.** Readjust the Gain control so that the higher peak from the ⁶⁰Co source (1.33 MeV) provides an amplifier output at ~9 V.

4.5. POLE-ZERO ADJUSTMENT

The pole-zero adjustment is extremely critical for good performance at high count rates. This adjustment should be checked carefully for the best possible results.

USING A GERMANIUM SYSTEM AND ⁶⁰Co

- **a.** Adjust the radiation source count rate between 2 kHz and 10 kHz.
- **b.** Observe the unipolar output with an oscilloscope. Adjust the PZ ADJ control so that the pulse trailing edges return to the baseline without overshoot or undershoot (Fig. 4.3).

The oscilloscope used must be dc coupled and must not contribute distortion in the observed waveforms. Oscilloscopes such as Tektronix 453, 454,465, and 475 will overload for a 10-V signal when the vertical sensitivity is <100 mV/cm. To prevent overloading the oscilloscope, use the clamp circuit shown in Fig. 4.4.



Fig. 4.3. Typical Waveform Illustrating Pole-Zero Adjustment Effects; Oscilloscope Trigger, Internal Pos., 60Co Source with 1.33-MeV Peak Adjusted ~9 V; Count Rate 3 kHz; Shaping Time Constant 1.5 µs.



Fig. 4.4. A Clamp Circuit That Can Be Used to Prevent Overloading the Oscilloscope Input.

USING SQUARE WAVE THROUGH PREAMPLIFIER TEST INPUT

A more precise pole-zero adjustment in the 855 can be obtained by using a square wave signal as the input to the preamplifier. Many oscilloscopes include a calibration output on the front panel, and this is a good source of square wave signals at a frequency of ~1 kHz. The amplifier differentiates the signal from the preamplifier so that it generates output signals of alternate polarities on the leading and trailing edges of the square wave input signal, and these can be compared as shown in Fig. 4.5 to achieve excellent pole-zero cancellation. Use the following procedure:

- a. Remove all radioactive sources from the vicinity of the detector. Set up the system as for normal operation, including detector bias.
- **b.** Set tire 855 controls as for normal operation; this includes gain, shaping, and input polarity.
- **c.** Connect the source of 1-kHz square waves through an attenuator to the Test input of the preamplifier. Adjust the attenuator so that the 855 output amplitude is -9 V.
- **d.** Observe the Unipolar output of the 855 with an oscilloscope. Adjust the PZ ADJ control for proper response according to Fig. 4.5. Use the clamp circuit in Fig. 4.4 to prevent overloading the oscilloscope input.



A. PZ Properly Adjusted; Slow Trigger to Separate Puises.





C. Undercompensated.

Fig. 4.5. Pole-Zero Adjustment Using a Square Wave Input to the Preamplifier.

4.6. OPERATION WITH SEMICONDUCTOR DETECTORS

CALIBRATION OF TEST PULSER

An ORTEC 419 Precision Pulse Generator (or equivalent) is easily calibrated so that the maximum pulse height dial reading (1000 divisions) is equivalent to 10-MeV loss in a silicon radiation detector. The procedure is as follows:

- a. Connect the detector to be used to the spectrometer system; that is, preamplifier, main amplifier, and biased amplifier.
- **b.** Allow excitation from a source of known energy (e.g., alpha particles) to fall on the detector.
- c. Adjust the amplifier gain and the bias level of the biased amplifier to give a suitable output pulse.
- d. Set the pulser Pulse Height control at the energy of the alpha particles striking the detector (e.g., set the dial at 547 divisions for a 5.47-MeV alpha particle energy).
- E. Turn on the pulser and use its Normalize control and attenuators to set the output due to the pulser for the same pulse height as the pulse obtained in step c. Lock the Normalize control and do not move it again until recalibration is required.

The pulser is now calibrated; the Pulse Height dial reads directly in MeV if the number of dial divisions is divided by 100.

AMPLIFIER NOISE AND RESOLUTION MEASUREMENTS

As shown in Fig. 4.6, a preamplifier, amplifier, pulse generator, oscilloscope, and wide-band rms voltmeter such as the Hewlett-Packard 3400A are required for this measurement. Connect a suitable capacitor to the input to simulate the detector capacitance desired. To obtain the resolution spread due to amplifier noise:

a. Measure the rms noise voltage (E_{rms}) at the amplifier output.



Fig. 4.6. System for Measuring Amplifier and Detector Noise Resolution.

b. Turn on the 4I9 precision pulse generator and adjust the pulser output to any convenient readable voltage, E_o, as determined by the oscilloscope.

The full-width-half-maximum (FWHM) resolution spread due to amplifier noise is then

$$N(FWHM) = \frac{2.35 \ E_{rms} \ E_{dial}}{E_o}$$

where E_{dial} is the pulser dial reading in MeV, and 2.35 is the factor for rms to FWHM. For average-responding voltmeters such as the Hewlett-Packard 400D, the measured noise must be multiplied by 1.13 to calculate the rms noise.

The resolution spread will depend on the total input capacitance, because the capacitance degrades the signal-to-noise ratio much faster than the noise.

DETECTOR NOISE-RESOLUTION MEASUREMENTS

The measurement just described can be made with a biased detector instead of the external capacitor that would be used to simulate detector capacitance. The resolution spread will be larger because the detector contributes both noise and capacitance to the input. The detector noiseresolution spread can be isolated from the amplifier noise spread if the detector capacitance is known, since

$$(N_{Det})^2 + (N_{Elec})^2 = (N_{Total})^2$$

where N_{total} is the total resolution spread, and N_{elec} is the electronic resolution spread when the detector is replaced by its equivalent capacitance.

The detector noise tends to increase with bias voltage, while the detector capacitance decreases. The net change in resolution spread will depend upon which effect is dominant. Figure 4.7 shows curves of typical noise-resolution spread versus bias voltage using data from several ORTEC silicon surface-barrier semi-conductor radiation detectors.



Fig. 4.7. Noise as a Function of Bias Voltage.

A M P L I F I E R NOISE-RESOLUTION MEASUREMENTS USING MCA

Probably the most convenient method of making resolution measurements is with a pulse height analyzer, as shown by the setup illustrated in Fig. 4.8.



Fig. 4.8. System for Measuring Resolution with a Pulse Height Analyzer.

The electronic noise-resolution spread can be measured directly with a pulse height analyzer and the mercury pulser as follows:

- a. Select the energy of interest with an ORTEC 419 Precision Pulse Generator. Set the amplifier and biased amplifier gain and bias level controls so that the energy is in a convenient channel of the analyzer.
- b. Calibrate the analyzer in keV per channel, using the pulser; full scale on the pulser dial is 10 MeV when calibrated as described above.
- c. Obtain the amplifier noise-resolution spread by measuring the FWHM of the pulser peak in the spectrum.

The detector noise-resolution spread for a given detector bias can be determined in the same manner by connecting a detector to the preamplifier input. The amplifier noise resolution spread must be subtracted as described in "Detector Noise-Resolution Measurements." The detector noise will vary with detector size and bias conditions and possibly with ambient conditions.

CURRENT VOLTAGE MEASUREMENTS FOR Si AND Ge DETECTORS

The amplifier system is not directly involved in semiconductor detector current voltage measurements, but the amplifier serves to permit noise monitoring during the setup. The detector noise measurement is a more sensitive method than a current measurement of determining the maximum detector voltage that should be used because the noise increases more rapidly than the reverse current at the onset of detector breakdown. Make this measurement in the absence of a source.

Figure 4.9 shows the setup required for current voltage measurements. An ORTEC 428 Bias Supply is used as the voltage source. Bias voltage should be applied slowly and reduced when noise increases rapidly as a function of applied bias. Figure 4.10 shows several typical current voltage curves for ORTEC silicon surface-barrier detectors.

When it is possible to float the microammeter at the detector bias voltage, the method of detector current measurement shown by the dashed lines in Fig. 4.9 is preferable. The detector is grounded as

in normal operation, and the microammeter is connected to the current monitoring jack on the 428 detector supply.



Fig. 4.9. System for Detector Current and Voltage Measurements.



Fig. 4.10. Silicon Detector Back Current vs Bias Voltage.

4.7. OPERATION IN SPECTROSCOPY SYSTEMS

HIGH-RESOLUTION ALPHA-PARTICLE SPECTROSCOPY SYSTEM

The block diagram of a hig-resolution spectroscopy system for measuring natural alpha particle radiation is shown in Fig. 4.11. Since natural alpha radiation occurs only above several MeV, an ORTEC 444 Biased Amplifier is used to suppress the unused portion of the spectrum; the same result can be obtained by using digital suppression on the MCA in many cases. Alpha-particle resolution is obtained in the following manner:

a. Use appropriate amplifier gain and minimum biased amplifier gain and bias level.

Accumulate the alpha peak in the MCA.

- **b.** Slowly increase the bias level and biased amplifier gain until the alpha peak is spread over 5 to 10 channels and the minimum- to maximum-energy range desired corresponds to the first and last channels of the MCA.
- c. Calibrate the analyzer in keV per channel using the purser and the known energy of the alpha peak (see "Calibration of Test Pulser") or two known-energy alpha peaks.
- **d.** Calculate the resolution by measuring the number of channels at the FWHM level in the peak and converting this to keV.

HIGH-RESOLUTION GAMMA SPECTROSCOPY SYSTEM

A high-resolution gamma spectroscopy system block diagram is shown in Fig. 4.12. Although a biased amplifier is not shown (an analyzer with more channels being preferred), it can be used if the only analyzer available has fewer channels and only higher energies are of interest.

When a germanium detector that is cooled by a liquid nitrogen cryostat is used, it is possible to obtain resolutions from about 1 keV FWHM up (depending on the energy of the incident radiation and the size and quality of the detector). Reasonable care is required to obtain such results. Some guidelines for obtaining optimum resolution are:

- a. Keep interconnection capacitance between the detector and preamplifier to an absolute minimum (no long cables).
- **b.** Keep humidity low near the detectorpreamplifier junction.
- **c.** Operate the amplifier with the shaping time that provides the best signal-to-noise ratio.
- **d.** Operate at the highest allowable detector bias to keep the input capacitance low.



Fig. 4.11. System for High-Resolution Alpha-Particle Spectroscopy.



Fig. 4.12. System for High-Resolution Gamma Spectroscopy.

SCINTILLATION-COUNTER GAMMA SPECTROSCOPY SYSTEMS

The ORTEC 855 can be used in scintillationcounter spectroscopy systems as shown in Fig. 4.13. The amplifier shaping time constants should be selected in the region of 0.5 to 1.5 μ s for Nal or plastic scintillators. For scintillators having longer decay times, longer time constants should be selected.

X-RAY SPECTROSCOPY USING PROPORTIONAL COUNTERS

Space charge effects in proportional counters, operated at high gas amplification, tend to degrade the resolution capabilities drastically at x-ray energies, even at relatively low counting rates. By using a high-gain, low-noise amplifying system and lower gas amplification, these effects can be reduced and a considerable improvement in resolution can be obtained.



Fig. 4.13. Scintillation-Counter Gamma Spectroscopy System.

The block diagram in Fig. 4.14 shows a system of this type. Analysis can be accomplished by simultaneous acquisition of all data on a multichannel analyzer or counting a region of. interest in a single-channel analyzer window with a counter and timer or counting ratemeter.

4.8. OTHER EXPERIMENTS

Block diagrams illustrating how 855 and other ORTEC modules can be used for experimental setups for various other applications are shown in Figs. 4.15, 4.16, and 4.17.



Fig. 4.14. High-Resolution X-Ray Energy Analysis System Using a Proportional Counter.



Fig. 4.15. Gamma-Ray Charged-Particle Coincidence Experiment.



Fig. 4.16. Gamma-Ray Pair Spectrometry.



Fig. 4.17. Gamma-Gamma Coincidence Experiment.

5. CIRCUIT DESCRIPTION

Figure 5.1 is a block diagram of the instrument. Much of the circuitry has been designed to use integrated circuits. This advanced technology achieves an economical high-density package with high reliability.

The signal passes through a differentiator, with a time constant that is selected by W5, to the inverting differential amplifier formed by transistors Q1 through Q6. The nominal gain of this stage is 4.5 Dc-coupling for the input is furnished through the pole-zero cancellation circuit; the current for this.circuit is a function of the setting of the PZ ADJ control (R1) and the resistance path selected by W5 and by the pulse that is being compensated.

Integrated circuits U4 and U2 are the gain stages. Feedback around each stage controls the gain. The gain of U4 is X1 or X1O as selected by Coarse Gain switch S2A. For U2, the fine gain is controlled by R30. Switch S2B allows selection of input resistors to vary the coarse gain.

The output of U2 is processed through an active integrator amplifier, U1, that provides a complex pole pair. The real pole of the differentiator at the input is 1.05 times the value of the real part of complex pole pair. The shaping time constant is selected by jumper W1, determining which, resistances are used to control the time constant.

Amplifier U1 is connected in a noninverting configuration with a dc gain of 1.42 as set by R3 and R4.

The output of U1 is inverted by the input polarity amplifier U3. The input polarity jumper, W3, selects either the output of U3 for a positive input or the noninverted output of U1 for a negative input pulse.

The output of W3 is furnished to the bipolar output stage, A3, through a differentiator. The time constant selected by W7 and W9 determines the capacitance in this ac-coupling, and the output of A3 is the signal that is provided through the B1

(Bipolar) connectors on the front and rear panels. Resistor R103 is the series $93-\Omega$ termination for the rear panel connection.

The output of W3 is also furnished to the unipolar output stage, A2. The signal is furnished to the input of A2 through R76 and R78. The hybrid circuit, A2, drives the output signal through the UNI (Unipolar) connectors on the front and rear panels. Resistor R102 is the series 93Ω termination for the rear panel connection.

Transistors Q10, Q11, Q12, and Q13 limit the average dc current through the output amplifiers to 35 mA.

The BLR (baseline restorer) circuit stabilizes the unipolar output stage within a few mV of zero determined by dc stability of U7. The BLR circuit consists of amplifier U7 and gated transconductance amplifier U6 with C30 and Q7. The correction is accumulated as a charge on C30 and applied through FET Q7 to A2. U6 is gated on during intervals when no input signal is present, and is gated off when a signal is present to prevent interference with the pulses of interest and to prevent the pulse amplitudes from affecting the average charge on C30. Gating is provided by the baseline discriminator U5. The gate permits correction between input signals by identifying when signals are present and preventing the correction during these intervals. The negative restorer gate threshold is set at -400 mV for use with resetting-type preamplifiers.

The unipolar output is amplified at U7 providing the input for U5, U6, and the Auto Noise Level (ANL) circuitry. When the signal is greater than the dc threshold set by the ANL, U5 turns on, driving Q8 to turn off current source Q9 that supplies the restorer current for U6. Hybrid A1 establishes an automatic BLR threshold level that is related to the output noise level.



Fig. 5.1. Amplifier Block Diagram.

6. MAINTENANCE

6.1. TEST EQUIPMENT REQUIRED

The following test equipment should be used to adequately test the specifications of the 855 amplifier:

- 1. ORTEC 419 Precision Pulse Generator or 448 Research Pulser.
- 2. Tektronix 547 Series Oscilloscope with a type 1A1 plug-in or equivalent.
- 3. Hewlett-Packard 3400A rms voltmeter.

6.2. PULSER TEST²

FUNCTIONAL CHECKS Set the 855 controls as follows:

Coarse Gain	100
Gain	7.5
Input Polarity	Neg
Shaping Time Constant	1.5 µs

a. Connect a positive pulser output to the 855 input and adjust the pulser to obtain +10 V at the unipolar output. This should require an input pulse of 13.3 rnV using a $100-\Omega$ terminator at the input. Adjust PZ if necessary.

- **b.** Change the input polarity jumper (W3) to Pos and then back to Neg while monitoring the output for a polarity inversion.
- c. Monitor the bipolar output for dc level of <± mV; pulse shape should be bipolar. Return to UNI.
- **d.** Recheck the output pulse amplitude and adjust if necessary to set it at +10 V with maximum gain. Decrease the Coarse Gain switch stepwise from 100 to 2 and ensure that the output amplitude changes by the appropriate amount for each step. Return the Coarse Gain switch to 100.
- e. Decrease the Gain control from 7.5 to 2.5 and check to see that the output amplitude decreases by a factor of 2. Return the Gain control to maximum at 7.5.
- f. With the shaping jumpers set for 1.5 μs, measure the time to the peak on the unipolar output pulse; this should be 3.3μs (or 2.2τ). Measure the time to baseline crossover of the bipolar output; this should be 5.0 μs (or 3.3τ).
- g. Change the shaping jumpers to 0.5 and $3 \mu s$ in turn. At each setting, check to see that the time to the unipolar peak is 2.2τ . Return the jumpers to $1.5 \mu s$.

²See IEEE Standards No. 301-1976.

OVERLOAD TESTS Start with maximum gain, $T = 1.5 \mu s$, and a +10V output amplitude. Increase the pulser output amplitude by X200 and observe that the unipolar output returns to within 200 mV of the baseline within 24 µs after the application of a single pulse from the pulser. It will probably be necessary to vary the PZ ADJ control on the front panel in order to cancel the pulser pole and minimize the time required for return to the baseline.

LINEARITY The integral nonlinearity of the 855 can be measured by the technique shown in Fig. 6.1. In effect, the negative pulser output is subtracted from the positive amplifier output to cause a null point that can be measured with excellent sensitivity. The pulser output must be varied between 0 to 10 V, which usually requires an external control source for the pulser. The amplifier gain and the pulser attenuator must be adjusted to measure 0 V at the null point when the pulser output is 10 V. The variation in the null point as the pulser is reduced gradually from 10 V to 0 V is a measure of the nonlinearity. Since the subtraction network also acts as a voltage divider, this variation must be less than

- (10 V full scale) x (±0.05% maximum nonlinearity) x (1/2 for divider network)
 - = ± 2.5 mV for the maximum null-point variation.

OUTPUT LOADING Use the test setup of Fig. 6.1. Adjust the amplifier output to 10 V and observe the null point when the front panel output is terminated in 100 Ω . The change should be <2.5 mV.

NOISE Measure the noise at the amplifier unipolar output with maximum amplifier gain and $3-\mu s$ shaping time. Using a true-rms voltmeter, the noise should be <5 μ V x 750 (gain), or 3.75 mV.

For an average responding voltmeter, the noise reading would have to be multiplied by 1.13 to calculate the rms noise. The input must be terminated in 100 Ω during the noise measurements.

6.3. SUGGESTIONS FOR TROUBLESHOOTING

In situations where the 855 is suspected of a malfunction, it is essential to verify such malfunction in terms of simple pulse generator



Fig. 6.1. Circuit Used to Measure Nonlinearity.

impulses at the input. The unit must be disconnected from its position in any system and routine diagnostic analysis performed with a test pulse generator and an oscilloscope. It is imperative that testing not be performed with a source and detector until the amplifier performs satisfactorily with the test pulse generator.

The testing instructions in Section 6.2 and the circuit descriptions in Section 5 should provide assistance in locating the region of trouble and repairing the malfunction. The two side plates can be completely removed f rom the module to enable oscilloscope and, voltmeter observations.

6.4. FACTORY REPAIR

This instrument can be returned to the ORTEC factory for service and repair at a nominal cost. Our standard procedure for repair ensures the same quality control and checkout as for a new instrument. Always contact Customer Services at

ORTEC, (865) 482-4411, before sending in an instrument for repair to obtain shipping instructions. A Return Authorization Number is required and will be assigned to the unit. This number should be marked on the address label and on the package to ensure prompt attention when the unit reaches the factory.

6.5. TABULATED TEST POINT VOLTAGES

The voltages given in Table 6.1 are intended to indicate typical dc levels that can be measured on the PWB. In some cases the circuit will perform satisfactorily even though, due to component tolerances, there may be some voltage measurements which differ slightly from the listed values. The tabulated values should not be interpreted as absolute voltages but rather should be used as an aid during troubleshooting.

Table 6.1. Typical dc Voltages

Note: All voltages measured with no input signal, with the input terminated in 100 $\Omega,$ and all controls set fully clockwise at maximum.

Location	Voltage
TI	+-50 mv
T2	<u>+60 </u> mV
Т3	±0.7 V
T4	±1.0 v
T5	±60 rnV
T6	0 to -0.8 V
T7	<u>+6 </u> mV

Pin	Function	Pin	Function
1	+3 V	23	Reserved
2	-3 V	24	Reserved
3	Spare Bus	25	Reserved
4	Reserved Bus	26	Spare
5	Coaxial	27	Spare
6	Coaxial	*28	+24 V
7	Coaxial	*29	–24 V
8	200 V dc	30	Spare Bus
9	Spare	31	Spare
*10	+6 V	32	Spare
*11	-6 V	*33	117 V ac (Hot)
12	Reserved Bus	*34	Power Return Ground
13	Spare	35	Reset (Scaler)
14	Spare	36	Gate
15	Reserved	37	Reset (Auxiliary)
*16	+12 V	38	Coaxial
*17	–12 V	39	Coaxial
18	Spare Bus	40	Coaxial
19	Reserved Bus	*41	117 V ac (Neutral)
20	Spare	*42	High-Quality Ground
21	Spare	G	Ground Guide Pin
22	Reserved		

Bin/Module Connector Pin Assignments for Standard Nuclear Instrument Modules per DOE/ER-0457T

Pins marked (*) are installed and wired in ORTEC's Model 4001A and 4001C Modular System Bins.